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April 9, 2002

Commissioner for Patents
Washington, D.C. 20231

Re: New U.S. Patent Application
Inventor: Fletcher et al.
Entitled: METHODS AND DEVICES FOR OPTIMIZING POWER
CONSUMPTION OF TRIP UNITS CONTAINING
MICROPROCESSORS
Filed: March 20, 2002
Serial No.: 10/063,100
Attorney Docket No: 41PR-09032

Sir:

We enclose herewith:

- ☒ Information Disclosure Citation under 37 CFR 1.97(b)
- ☒ Copy of 4 references
- ☒ Acknowledgement postcard

The Commissioner is hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 06-1130.

Applicant hereby petitions under 37 CFR 1.136 and other applicable rules to have the response period extended the number of months necessary to render the attached communication timely in the event a petition is required.

Respectfully submitted,

Edward L. McMahon
Reg. No. 44,927

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, Washington D.C. 20231, on April 9, 2002.

Beatriz E. Guerra

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